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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete if Known

Application Number	09/510375
Filing Date	February 22, 2000
First Named Inventor	Williams, Brett
Group Art Unit	2187
Examiner Name	Kim, Hong

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Sheet 1 of 1

Attorney Docket No: 303.164US3

Technology Center 2100

US PATENT DOCUMENTS

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
RLH	US-5,654,932	08/05/1997	Rao, G.R.M.	365	230.03	10/04/1995

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²
RLH	WO-96/20482	07/04/1996	Zagar, P. , et al	G11 C	7/00	

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
RLH		"Synchronous DRAM 2 MEG X 8 SDRAM", Micron Semiconductors, Inc., (1994), pp. 1-18	

EXAMINER

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Substitute Disclosure Statement Form (PTO-1449)

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